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Docket Number (Optional) SUN-P5403

In re Application of: Manjunath D. Haritsa et al.

DEC 1 5 2003

Application No. 09/982,452 Filed: October 17, 2001

eation No. 09/982,452

TECHNOLOGY CENTER 2800

For: Clock Skew Verification Methodology for Grid-Based Desig**SPECIAL PROGRAM CENTER**The owner*, <u>Sun Microsystems, Inc.</u> of <u>100</u> percent interest in the instant application hereby disclaims, except as covided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend

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In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 to 156 and 173 of any patent granted on the second application, as shortened by any terminal disclaimer filed prior to the patent grant, in the event that any such granted patent: expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims cancelled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as shortened by any terminal disclaimer filed prior to its grant.

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, David B. Ritchie, Reg. No. 31,562

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Application Number		Applic ()	
	09/982,452	HARITSA ET AL.	

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TERMINAL DISCLAIMER APPLICATION: 09 982,452	
RE: TERMINAL DISCLAIMER:	· · · · · · · · · · · · · · · · · · ·
RELATED APPLICATION(S) 19 982,459	HAS
(HAVE) NOT ISSUED AS OF 12-15-2013 DO NOT REMOVE NOTE UNTIL 09/982,452 HAS BEEN ISSUED AND DOCUMENTED IN FILE 09/982,459.	